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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/066,061	04/24/1998	MATTHEW ZAVRACKY	KPN97-04A2	8310

21005 7590 04/15/2003

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EXAMINER

NGUYEN, JIMMY H

ART UNIT PAPER NUMBER

2673

DATE MAILED: 04/15/2003

28

Please find below and/or attached an Office communication concerning this application or proceeding.

TD

**Advisory Action**

Application No.

09/066,061

Applicant(s)

ZAVRACKY ET AL. 

Examiner

Jimmy H. Nguyen

Art Unit

2673

**--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --**

THE REPLY FILED 28 March 2003 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE. Therefore, further action by the applicant is required to avoid abandonment of this application. A proper reply to a final rejection under 37 CFR 1.113 may only be either: (1) a timely filed amendment which places the application in condition for allowance; (2) a timely filed Notice of Appeal (with appeal fee); or (3) a timely filed Request for Continued Examination (RCE) in compliance with 37 CFR 1.114.

**PERIOD FOR REPLY** [check either a) or b)]

- a) ☒ The period for reply expires 8 months from the mailing date of the final rejection.
- b) ☐ The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection. ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

1. ☒ A Notice of Appeal was filed on 23 December 2002. Appellant's Brief must be filed within the period set forth in 37 CFR 1.192(a), or any extension thereof (37 CFR 1.191(d)), to avoid dismissal of the appeal.
2. ☐ The proposed amendment(s) will not be entered because:
- (a) ☐ they raise new issues that would require further consideration and/or search (see NOTE below);
- (b) ☐ they raise the issue of new matter (see Note below);
- (c) ☐ they are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or
- (d) ☐ they present additional claims without canceling a corresponding number of finally rejected claims.

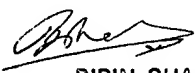
NOTE: \_\_\_\_\_

3. ☒ Applicant's reply has overcome the following rejection(s): the rejection under 35 USC112, second paragraph.
4. ☐ Newly proposed or amended claim(s) \_\_\_\_\_ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).
5. ☒ The a) ☐ affidavit, b) ☐ exhibit, or c) ☒ request for reconsideration has been considered but does NOT place the application in condition for allowance because: See Continuation Sheet.
6. ☐ The affidavit or exhibit will NOT be considered because it is not directed SOLELY to issues which were newly raised by the Examiner in the final rejection.
7. ☒ For purposes of Appeal, the proposed amendment(s) a) ☐ will not be entered or b) ☒ will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.

The status of the claim(s) is (or will be) as follows:

Claim(s) allowed: NONE.Claim(s) objected to: NONE.Claim(s) rejected: 14,16,17,25-29 and 37-39.Claim(s) withdrawn from consideration: NONE.

8. ☐ The proposed drawing correction filed on \_\_\_\_\_ is a) ☐ approved or b) ☐ disapproved by the Examiner.
9. ☐ Note the attached Information Disclosure Statement(s) (PTO-1449) Paper No(s). \_\_\_\_\_.
10. ☐ Other: \_\_\_\_\_



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Continuation of 5. does NOT place the application in condition for allowance because: of the same reasons set forth in the last Office Action. Applicants' argument filed, "none of these references teaches or suggests the Applicants' switching circuit, under the direction of a timing circuit, that switches a common voltage applied to a liquid crystal display", page 6, last second line through page 7, line 1, has been fully considered but it is not persuasive because as discussed in the last Office Action, Kitazima et al implicitly discloses, under the direction of an inherent timing circuit (i.e., the circuit for providing control signals M, CP, FST and LST, and image data D, see fig. 6), a switching circuit (a counter electrode terminal voltage receiver 44, see fig. 6) switches a common voltage (VCM) between  $(V_c+V_b)$  during the pixel ON and  $(V_c-V_b)$  during pixel OFF (or erasing pixel), applied to LCD (see fig. 7). For the reason above, the rejection is maintained